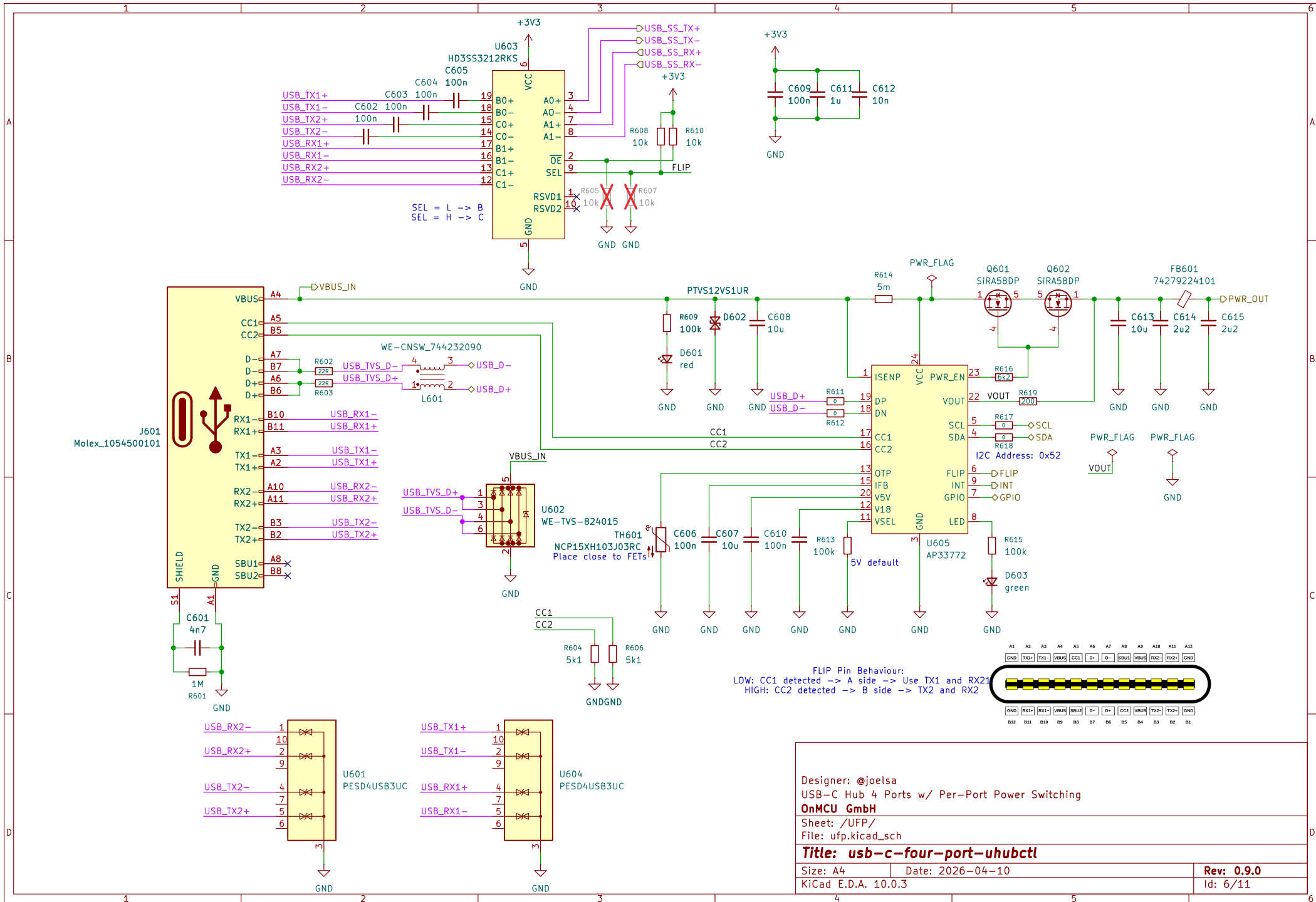


Designer: @joelsa  
 USB-C Hub 4 Ports w/ Per-Port Power Switching  
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Sheet: /  
 File: usb-c-hub-four-port-uhubctl.kicad\_sch

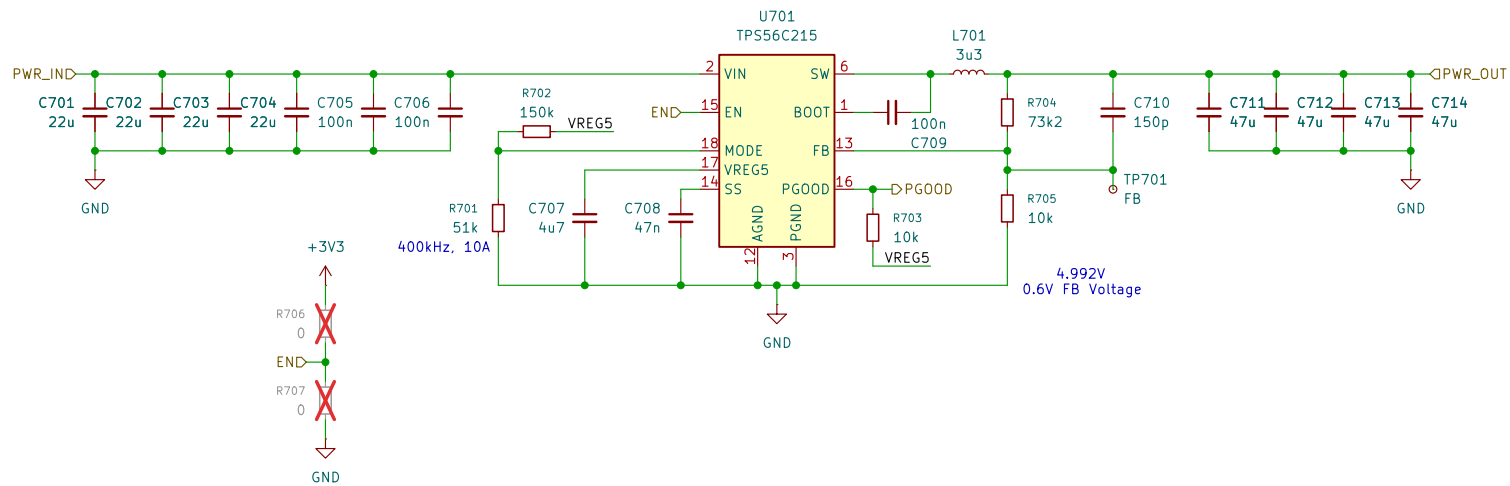
**Title: usb-c-four-port-uhubctl**

Size: A4	Date: 2026-04-10	Rev: 0.9.0
KiCad E.D.A. 10.0.3		Id: 1/11



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 USB-C Hub 4 Ports w/ Per-Port Power Switching  
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 Sheet: /UFP/  
 File: ufp.kicad\_sch

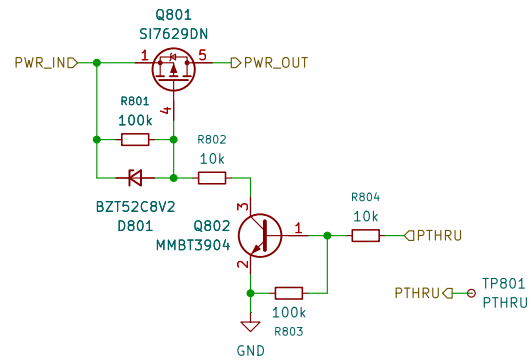
<b>Title: usb-c-four-port-uhubctl</b>	
Size: A4	Date: 2026-04-10
KiCad E.D.A. 10.0.3	Rev: 0.9.0 Id: 6/11



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 USB-C Hub 4 Ports w/ Per-Port Power Switching  
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 Sheet: /BUCK\_5V/  
 File: buck\_5v.kicad\_sch

**Title: usb-c-four-port-uhubctl**

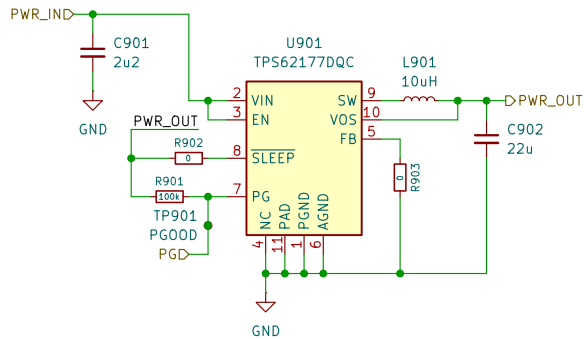
Size: A4	Date: 2026-04-10	Rev: 0.9.0
KiCad E.D.A. 10.0.3		Id: 7/11



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 USB-C Hub 4 Ports w/ Per-Port Power Switching  
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 Sheet: /Passthrough/  
 File: passsthrough.kicad\_sch

**Title: usb-c-four-port-uhubctl**

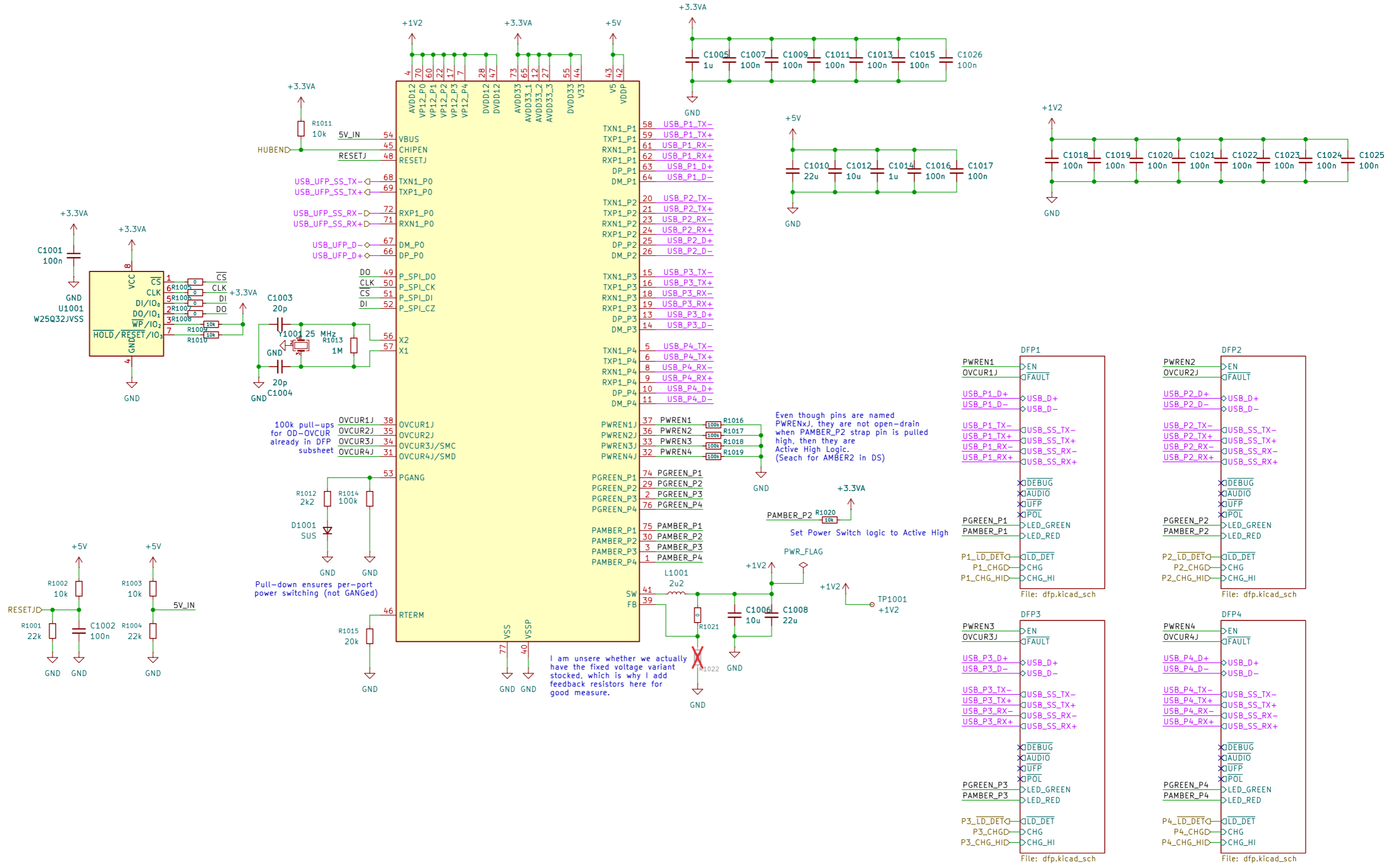
Size: A4	Date: 2026-04-10	Rev: 0.9.0
KiCad E.D.A. 10.0.3		Id: 8/11



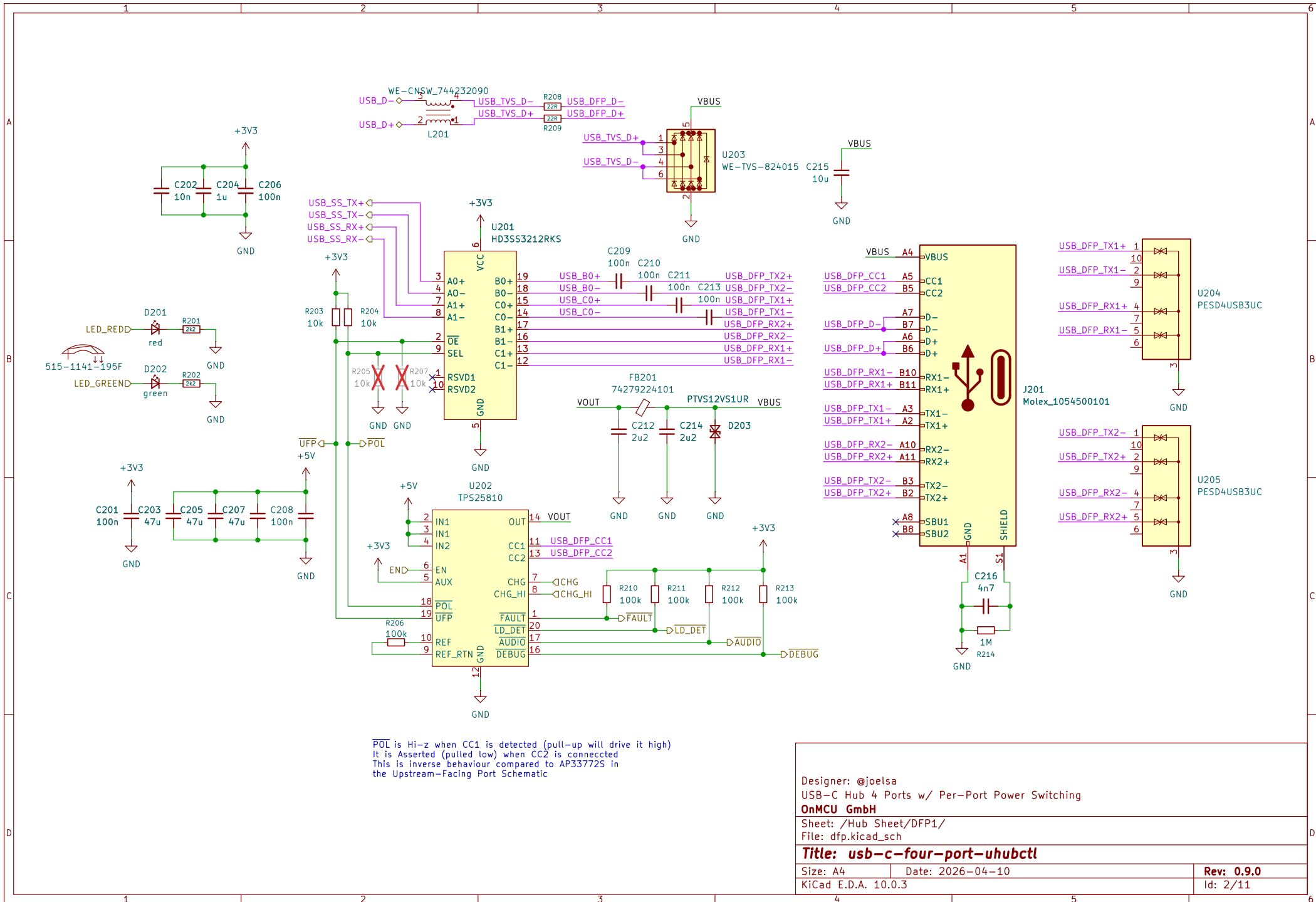
Designer: @joelsa  
 USB-C Hub 4 Ports w/ Per-Port Power Switching  
**OnMCU GmbH**  
 Sheet: /BUCK\_3V3/  
 File: buck\_3v3.kicad\_sch

**Title: usb-c-four-port-uhubctl**

Size: A5	Date: 2026-04-10	<b>Rev: 0.9.0</b>
KiCad E.D.A. 10.0.3		Id: 9/11

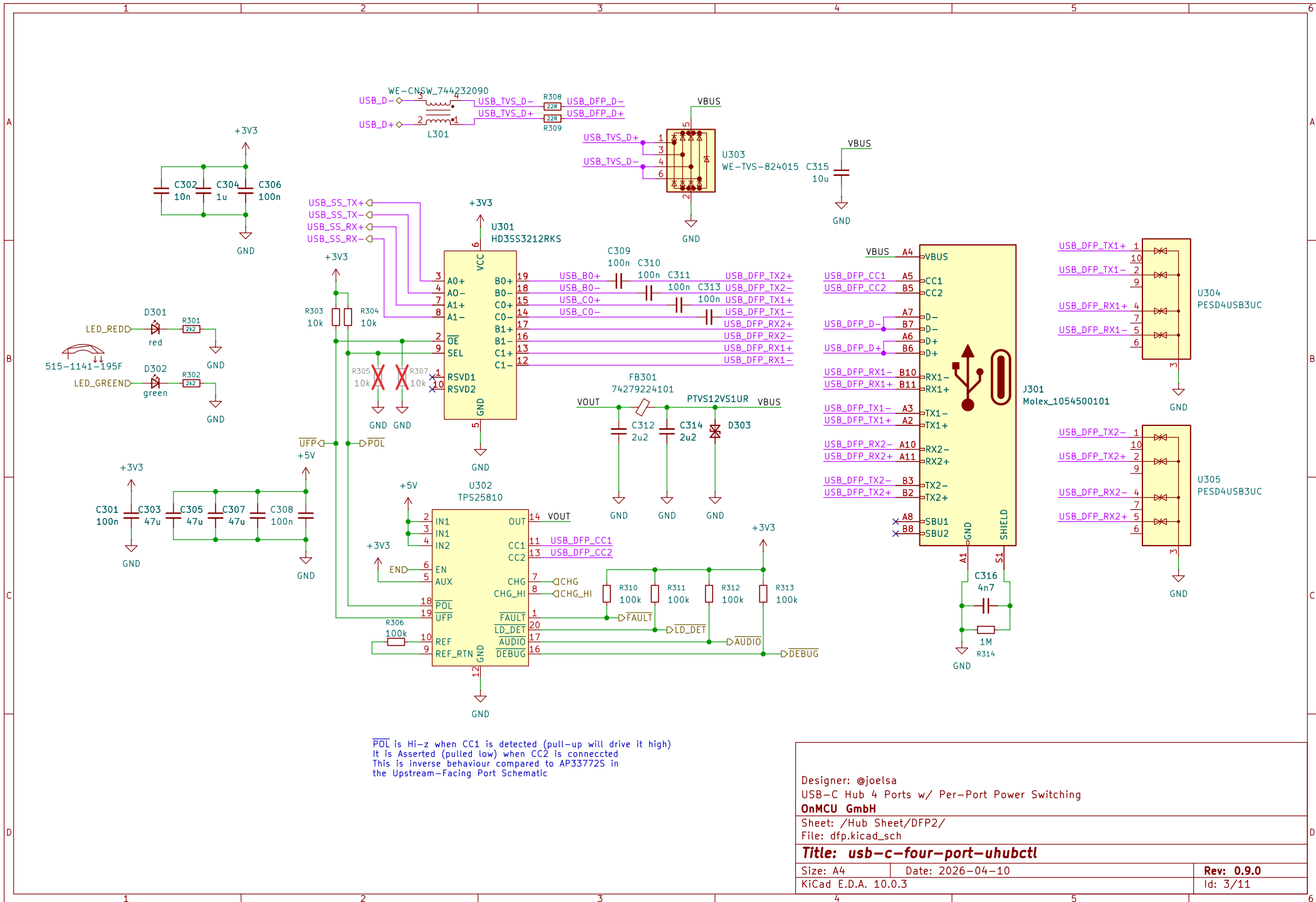


Designer: @joelsa		
USB-C Hub 4 Ports w/ Per-Port Power Switching		
OnMCU GmbH		
Sheet: /Hub Sheet/		
File: hub.kicad_sch		
<b>Title: usb-c-four-port-uhubctl</b>		
Size: A3	Date: 2026-04-10	Rev: 0.9.0
KiCad E.D.A. 10.0.3		Id: 10/11



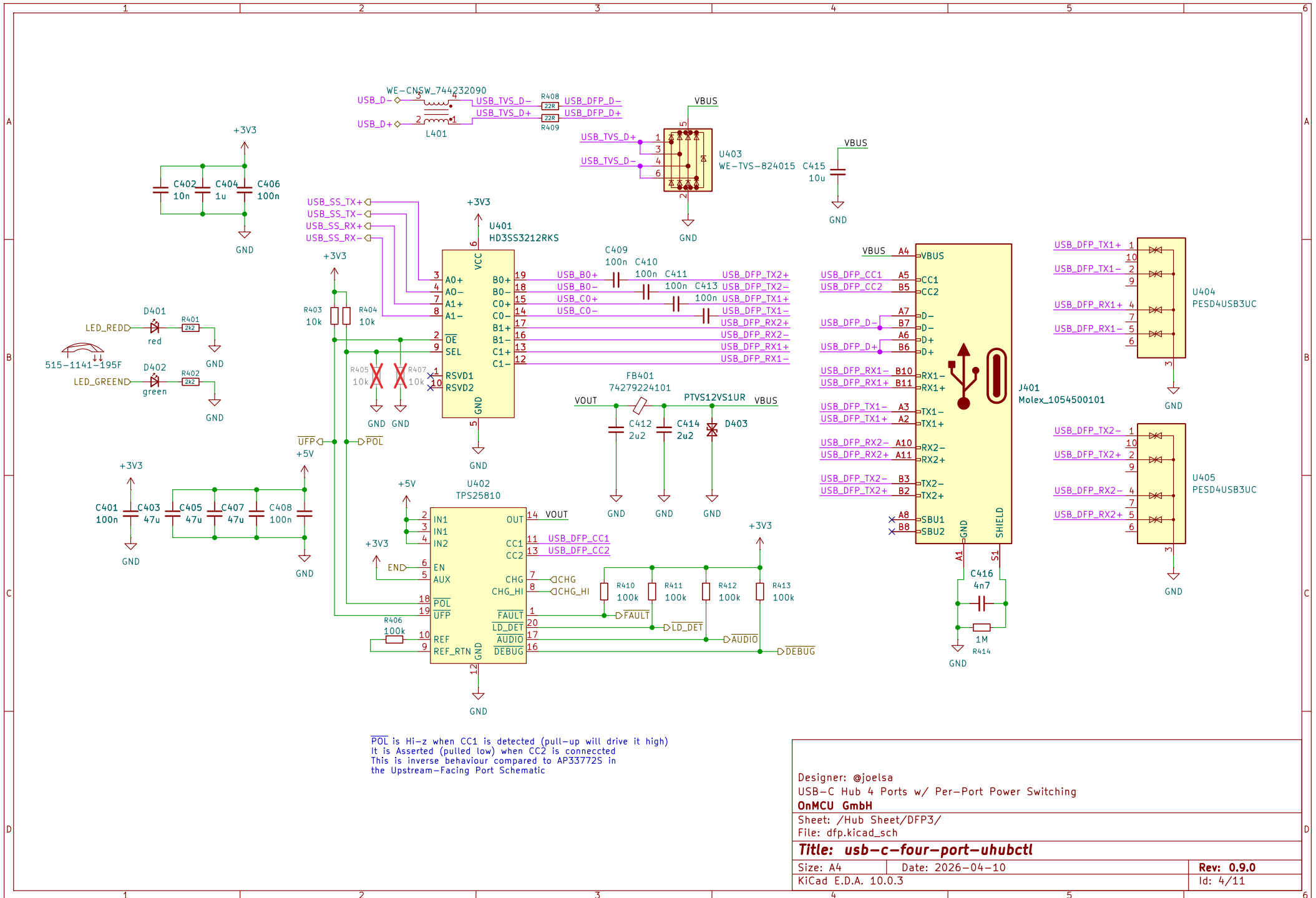
POL is Hi-z when CC1 is detected (pull-up will drive it high)  
 It is Asserted (pulled low) when CC2 is connected  
 This is inverse behaviour compared to AP33772S in  
 the Upstream-Facing Port Schematic

Designer: @joelsa	
USB-C Hub 4 Ports w/ Per-Port Power Switching	
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Sheet: /Hub Sheet/DFP1/	
File: dfp.kicad_sch	
<b>Title: usb-c-four-port-uhubctl</b>	
Size: A4	Date: 2026-04-10
KiCad E.D.A. 10.0.3	Rev: 0.9.0
	Id: 2/11



POL is Hi-z when CC1 is detected (pull-up will drive it high)  
 It is Asserted (pulled low) when CC2 is connected  
 This is inverse behaviour compared to AP33772S in  
 the Upstream-Facing Port Schematic

Designer: @joelsa	
USB-C Hub 4 Ports w/ Per-Port Power Switching	
OnMCU GmbH	
Sheet: /Hub Sheet/DFP2/	
File: dfp.kicad_sch	
<b>Title: usb-c-four-port-uhubctl</b>	
Size: A4	Date: 2026-04-10
KiCad E.D.A. 10.0.3	Rev: 0.9.0
	Id: 3/11



POL is Hi-Z when CC1 is detected (pull-up will drive it high)  
 It is Asserted (pulled low) when CC2 is connected  
 This is inverse behaviour compared to AP33772S in  
 the Upstream-Facing Port Schematic

Designer: @joelsa	
USB-C Hub 4 Ports w/ Per-Port Power Switching	
OnMCU GmbH	
Sheet: /Hub Sheet/DFP3/	
File: dfp.kicad_sch	
<b>Title: usb-c-four-port-uhubctl</b>	
Size: A4	Date: 2026-04-10
KiCad E.D.A. 10.0.3	Rev: 0.9.0
	Id: 4/11



